Sheet 1 of 4Substitute Form PTO-1449
(Mandatory)U.S. Department of Commerce
Patent and Trademark OfficeAttorney's Docket No.
10559-618001Application No.
10/057,738**Information Disclosure Statement
by Applicant**

(Use several sheets if necessary)

(37 CFR §1.98(b))

Applicant
Gilbert Wolrich et al.Filing Date
January 25, 2002Group Art Unit
2189**U.S. Patent Documents**

Examiner Initial	Desig. ID	Document Number	Publication Date	Patentee	Class	Subclass	Filing Date If Appropriate
SG	AA	2002/0053017	05/2002	Adiletta et al.			
	AB	2002/0056037	05/2002	Wolrich et al.			
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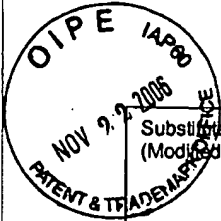
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Substitute Disclosure Form (PTO-1449)

Sheet 2 of 4Substitute Form PTO-1449
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(37 CFR §1.98(b))

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Examiner Initial	Desig. ID	Document Number	Publication Date	Patentee	Class	Subclass	Filing Date If Appropriate
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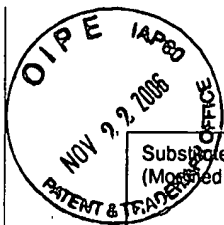
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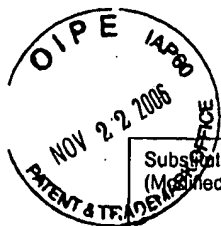


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							Yes	No
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	CU	WO 00/33195	06/2000	WIPO				
	CV	WO 01/16697	03/2001	WIPO				
	CW	WO 01/16698	03/2001	WIPO				
	CX	WO 01/16702	03/2001	WIPO				
	CY	WO 01/16703	03/2001	WIPO				
	CZ	WO 01/16713	03/2001	WIPO				

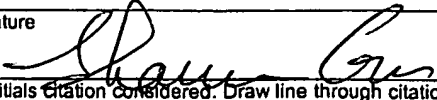
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Substitute Form PTO-1449 (Modified)	U.S. Department of Commerce Patent and Trademark Office	Attorney's Docket No. 10559-618001	Application No. 10/057,738
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		Filing Date January 25, 2002	Group Art Unit 2189

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							Yes	No
S.G.	DA	WO 01/16714	03/2001	WIPO				
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	DC	WO 01/16716	03/2001	WIPO				
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	DM	Hennessy et al., "Computer Organization and Design: The Hardware/Software Interface," Morgan Kaufman Publishers, 1998, pp. 116-119, 181-182, 225-227, 466-470, 476-482, 510-519, 712.
	DN	Keckler et al., "Exploiting fine grain thread level parallelism on the MIT multi-ALU processor," IEEE, June 1998.
	DO	Mendelson A. et al., "Design Alternatives of Multithreaded Architecture", <i>International Journal of Parallel Programming</i> , Plenum Press, New York, 27(3):161-193, June 1999.
	DP	Wadler, "The Concatenate Vanishes," University of Glasgow, December 1987 (revised November 1989), pp. 1-7.
	DQ	Waldspurger et al., "Register Relocation: Flexible Contents for Multithreading," Proceedings of the 20th Annual International Symposium on Computer Architecture," 1993, pp. 120-130.

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U.S. Patent Documents

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	AB						
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Foreign Patent Documents or Published Foreign Patent Applications

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							Yes	No
S.G.	AL							
	AM							
	AN							
	AO							
	AP							

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Examiner Initial	Desig. ID	Document
S.G.	AQ	Intel, "1A-64 Application Developer's Architecture Guide," Rev.1.0, May 1999, pp. 2-2, 4-29 to 4-31, 7-116 to 7-118 and c-21.
S.G.	AR	Paver et al., "Register Locking in Asynchronous Processor Computer Design: VLSI Processors," ICCD '92 Proceedings, IEEE 1992 International Conference, 1992, pp. 351-355.
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Examiner Signature <i>Shawn B...</i>	Date Considered 1/12/2007
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